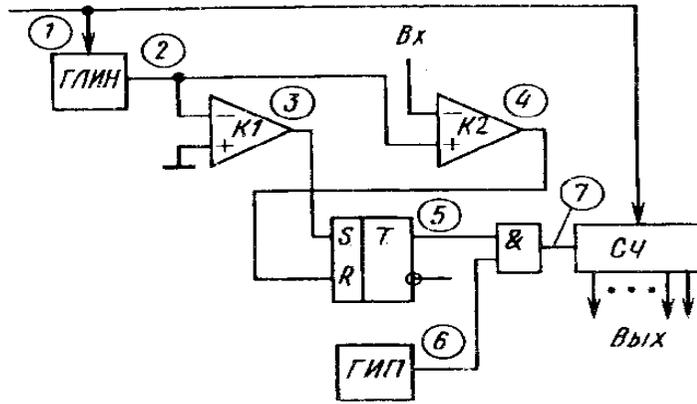


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4 De Bock, M.; Rombouts, P. Digital bilinear feedback for low-power double-sampling sigma-delta modulators. ELECTRONICS LETTERS Volume: 51 Issue: 1 Pages: 27-U57 Published: JAN 8 2015

5 G. Dhanabalan, S. Tamil Selvi. Design of parallel conversion multichannel analog to digital converter for scan time reduction of programmable logic controller using FPGA Computer Standards & Interfaces Volume 39, March 2015, Pages 12–21

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